

RECEIVED
CENTRAL FAX CENTER

JUN 28 2004

AMENDMENTS TO THE CLAIMS

The following listing of claims will replace all prior versions and listings of claims in the application.

OFFICIAL**LISTING OF CLAIMS**

1. (currently amended) A multiplexed address and data bus controlled by a bus master for communication between two microprocessors, comprising:

a plurality of multiplexed address and data lines in communication between the two microprocessors;

a read/write control signal line in communication between the two microprocessors for communicating whether a read or a write operation is to be performed;

a chip select line in communication between the two microprocessors, said chip select line transitioning to an enable condition during a data transfer cycle; and

a data strobe line in communication between the two microprocessors, said data strobe line providing a strobe each time a valid address and again each time valid data appears on said plurality of multiplexed address and data lines [[plurality of signals for each data transfer cycle wherein each data transfer cycle includes a]] wherein said strobe occurring during said time said valid address appears on said multiplexed address and data lines initiates one of a [[plurality of]] write and read sequence [[sequences which are initiated by said signals from said data strobe line]].

2. (cancelled)

Serial No. 09/838,975

3. (currently amended) A method of multiplexed address and data transfer for communicating between two microprocessors, comprising the steps of:

providing a chip select strobe signal to initiate a data transfer cycle and maintaining said chip select strobe signal during a duration of said data transfer cycle;

providing a read/write line qualifier signal to identify whether a read or a write data transfer operation is to be performed; and

providing a data strobe signal that steps one of the microprocessors through the data transfer cycle wherein address information is transferred during a first data strobe of said data strobe signal and at least one data string is transferred during subsequent data strobes of said data strobe signal.

4. (cancelled)

Serial No. 09/838,975